

Department of Computer Science and Information Engineering

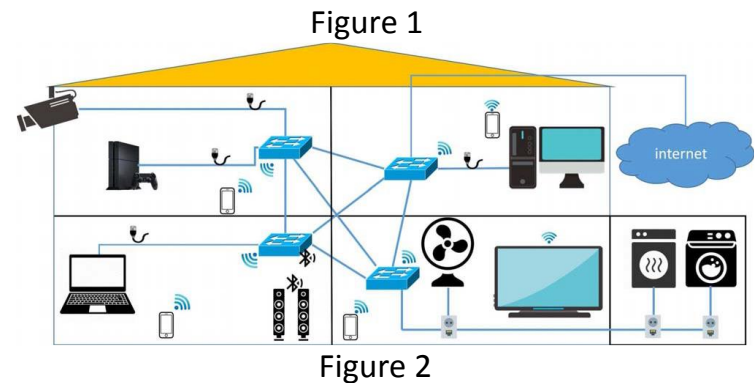
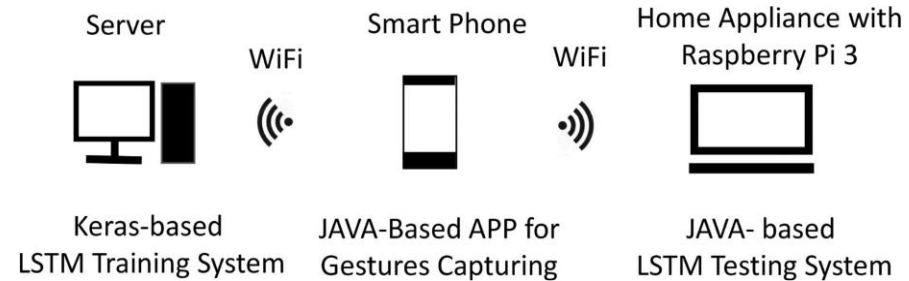
Multimedia Communication and System on Chip

My research interests mainly focus on the System-On-a-Chip, VLSI, Internet-of-Things, Smart Home and Artificial Intelligence. Our current projects include the sensor-based continuous hand gesture recognition, smart Quality of Service management, and AI hardware accelerator.

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Background
Ph.D., University of Massachusetts at Amherst.



Publications

- H. Y. Chen, W. J. Hwang, C. J. Cheng, X.-J. Lai, "An FPGA-Based Autofocusing Hardware Architecture for Digital Holography," *IEEE Trans. Computational Imaging*, Vol. 5, pp. 287-300, 2019.
- T. M. Tai, Y. J. Jhang, Z. W. Liao, K. C. Teng, W. J. Hwang, "Sensor-Based Continuous Hand Gesture Recognition by Long Short-Term Memory," *IEEE Sensors Letters*, Vol. 2, 6000704, 2018.
- W. J. Hwang, T. M. Tai, Y. J. Jhang, Y. C. Tung, C. H. Ho, and S. Y. Kuo, "Quality of Service Management for Home Networks Using Online Service Response Prediction," *IEEE Internet of Things Journal*, Vol. 4, pp. 1773-1786, 2017.

